DevComp Compensation Panel

DevComp power factor correction panels (PFC) are low voltage switchboards that correct the power factor (cos φ) by compensating, with capacitor steps, the reactive power the facility draws from the grid. They are designed in the 12.5 – 1000 kVAr range, with contactor-switched (standard) or thyristor-switched (dynamic) steps, and with detuned reactors in facilities with harmonic loads.

Technical Specifications

Applicable standard	IEC 61439-1, IEC 61439-2; IEC 61921 for power capacitor banks
Reactive power range	12.5 – 1000 kVAr (multiple panels for higher ratings)
Capacitor type	Self-healing, dry-type; cylindrical or box-type
Step switching	Capacitor-duty contactor (with pre-charge resistors to limit inrush current) or thyristor module
Control	Reactive power control relay; models with harmonic measurement and communication optional
Detuned reactor	Detuned (anti-resonant) reactor step option in facilities with harmonic loads
Step protection	Fused switch-disconnector or molded case circuit breaker per step
Thermal management	Thermostat-controlled filtered fans, planned air inlet and outlet, internal temperature monitoring
Measurement and display	cos φ, voltage, current, THD and step status display
Communication	Modbus RTU (optional), alarm output
Mounting	Floor-standing enclosure, placed next to the main switchboard or in a separate compensation room
Enclosure	Cold-rolled (DKP) sheet steel; electrostatic powder coating RAL 7035
IP rating	IP31 / IP41 / IP54
Maintenance support	Capacitance measurement, step testing and capacitor replacement

Certification: Design verified to IEC 61439-1 and -2 · ISO 9001:2015 quality management system · CE (Low Voltage Directive 2014/35/EU). IEC 61921 for power capacitor banks.